



S/N 10/750,582

PATENT

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant:	Vicki W. Tsai et al.	Examiner:	Fred O Ferris III
Serial No.:	10/750,582	Group Art Unit:	2128
Filed:	December 29, 2003	Docket:	80107.095US1
Title:	PROGRAMMING RECONFIGURABLE PACKETIZED NETWORKS		

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**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §§ 1.97 *et. seq.*, the referenced materials are brought to the attention of the Examiner for consideration in connection with the above-identified patent application. Applicants respectfully request that this Supplemental Information Disclosure Statement be entered and the documents listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to the provisions of MPEP 609, Applicants request that a copy of the 1449 form, initialed as being considered by the Examiner, be returned to the Applicants with the next official communication.

Pursuant to 37 C.F.R. §1.97(c)(2), Applicants have included the fee of \$180.00 as set forth in 37 C.F.R. §1.17(p). The Examiner is invited to contact the Applicants' Representative at the below-listed telephone number if there are any questions regarding this communication.

Pursuant to 37 C.F.R. 1.98(a)(2), Applicant believes that copies of cited U.S. Patents and Published Applications are no longer required to be provided to the Office. Notification of this change was provided in the United States Patent and Trademark Office OG Notices dated October 12, 2004. Thus, Applicant has not included copies of any US Patents or Published

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Dkt: P17896

Applications cited with this submission. Should the Office require copies to be provided, Applicant respectfully requests that notice of such requirement be directed to Applicant's below-signed representative. Applicant acknowledges the requirement to submit copies of foreign patent documents and non-patent literature in accordance with 37 C.F.R. 1.98(a)(2).

Respectfully submitted,

VICKI W. TSAI ET AL.

By their Representatives,

**Customer Number: 45445**

**Telephone Number: 952-473-8800**

Date December 11, 2006

By Dana B. LeMoine  
Dana B. LeMoine  
Reg. No. 40,062

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 11 day of December, 2006.

Jenny Hollingsworth  
Name

[Signature]  
Signature



PTO/SB/06a(07-05)  
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Substitute for form 1449A/PTO			
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>			
<i>(Use as many sheets as necessary)</i>			
<b>Complete If Known</b>			
<b>Application Number</b>		10/750,582	
<b>Filing Date</b>		December 29, 2003	
<b>First Named Inventor</b>		Tsai, Vicki	
<b>Art Unit</b>		2128	
<b>Examiner Name</b>		Ferris III, Fred O	
Sheet	1	of	2
Attorney Docket No: 80107.095US1			

US PATENT DOCUMENTS					
Examiner Initial *	Cite No	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Filing Date If Appropriate
		US-20040194048A1	09/30/2004	Arnold, J. M.	03/31/2003
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Examiner Initials*	Cite No	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	T <sup>2</sup>
		WO-05094016A2	10/06/2005	Honary, H. , et al.	
		WO-2005094016A3	10/06/2005	Honary, H.	

OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
		International Search Report and Written Opinion of the International Searching Authority; Dated 12/06/2005; PCT/US2005/010387, 16 pgs.	
		ALBANESI, M. G., et al., "A VLSI 128-Processor Chip for Multiresolution Image Processing", <u>Proceedings of the First International Conference on Ischia</u> , (May 02, 1994), 296-307	
		MARESCAUX, T. , et al., "Networks on Chip as Hardware Components of an OS for Reconfigurable Systems", <u>FIELD-PROGRAMMABLE LOGIC AND APPLICATIONS, 13TH INTERNATIONAL CONFERENCE</u> , Vol. 2778, (2003), 595-605	

EXAMINER

DATE CONSIDERED

Substitute Disclosure Statement Form (PTO-1449)  
\* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. <sup>1</sup> Applicant's unique citation designation number (optional) <sup>2</sup> Applicant is to place a check mark here if English language Translation is attached

Substitute for form 1449A/PTO  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>   (Use as many sheets as necessary)				<i>Complete If Known</i>	
				<b>Application Number</b>	10/750,582
				<b>Filing Date</b>	December 29, 2003
				<b>First Named Inventor</b>	Tsai, Vicki
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				<b>Examiner Name</b>	Ferris III, Fred O
Sheet	2	of	2	Attorney Docket No: 80107.095US1	

OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
		ZHANG, et al., "Interconnect Architecture Exploration for Low-Energy Reconfigurable Single-Chip DSPs", <u>VLSI, 99, PROCEEDINGS, IEEE COMPUTER SOCIETY WORKSHOP ON ORLANDO, FL, (April, 08, 1999), 2-8</u>	

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**DATE CONSIDERED**

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